

FIG. 1

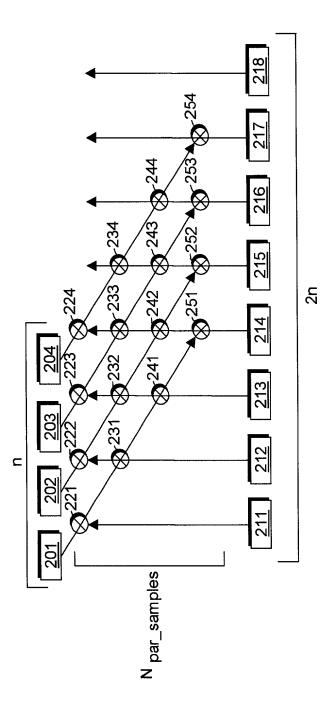
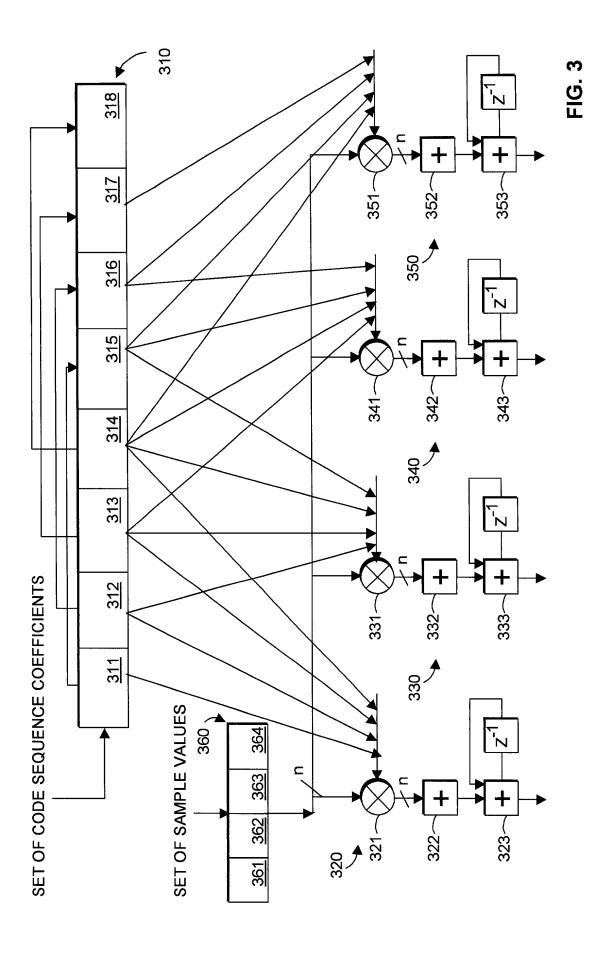
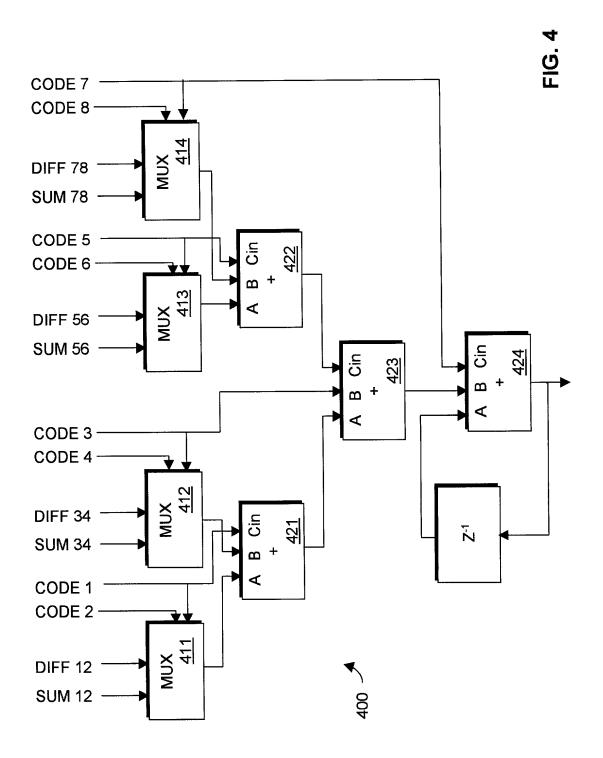


FIG. 2





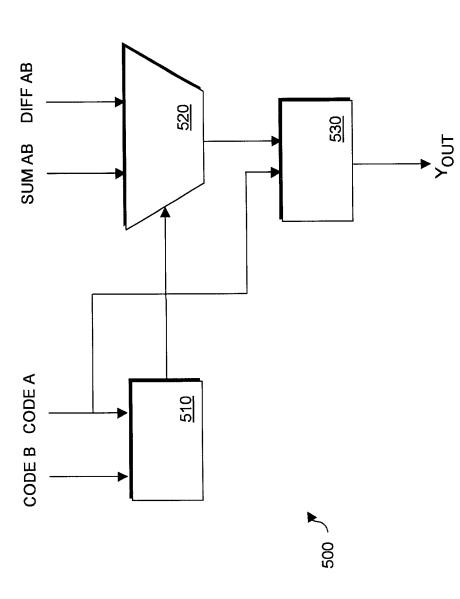
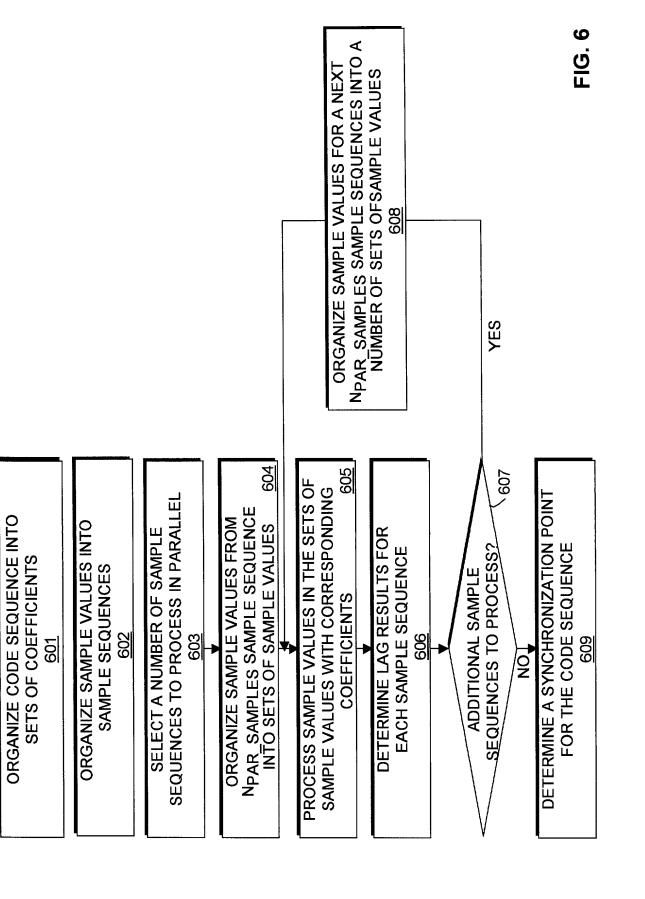


FIG. 5



LOAD SAMPLE VALUES FROM A FIRST SET OF SAMPLE VALUES INTO SAMPLE

SEQUENCE REGISTERS

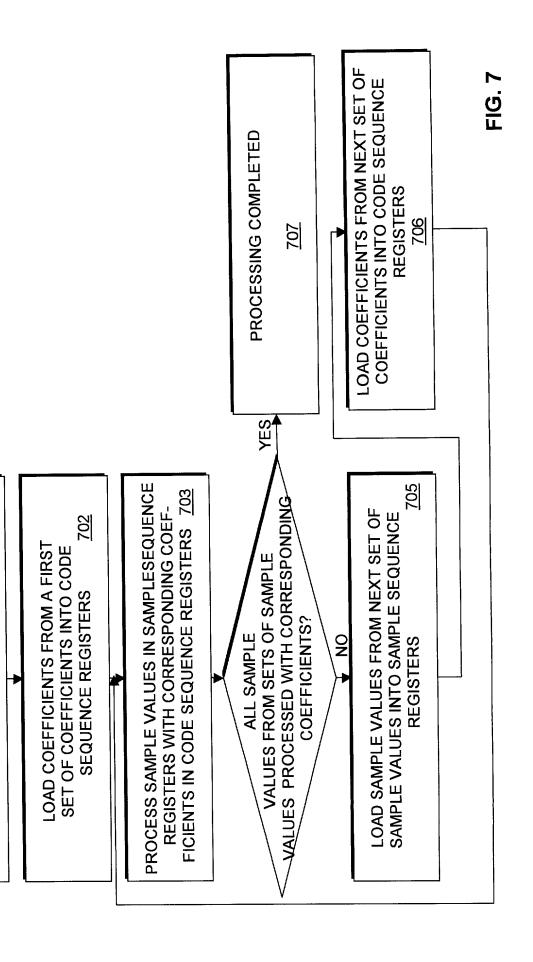


FIG. 8

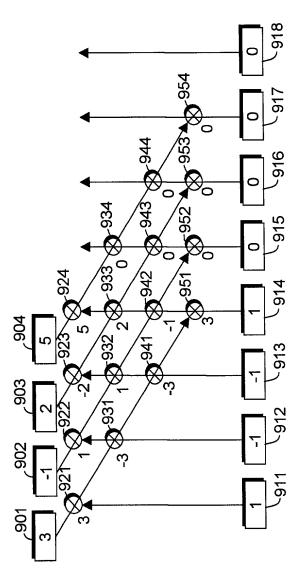


FIG. 9A

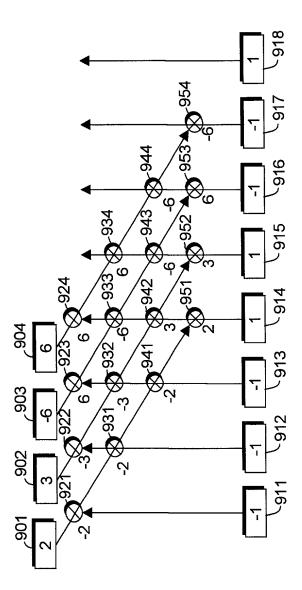


FIG. 9B

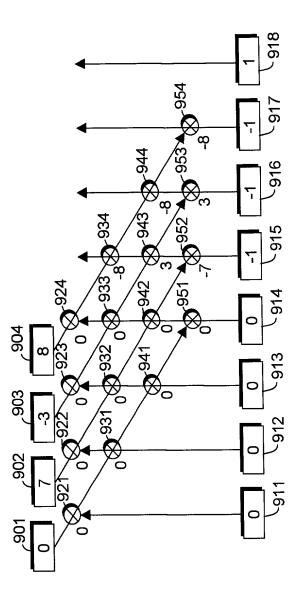


FIG. 9C

7

4

-50

-13

4

LAG RESULTS

FIG. 10

